

S12	1	multi?thread\$3 near4 (shift\$3)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2005/02/18 14:04
S13	0	multi?thread\$3 near4 ALU	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2005/02/18 14:19
S14	0	multi?thread\$3 near4 ((multiple or plurality) near4 (ALU\$1 or (arithmetic adj1 logic adj1 unit\$1)))	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2005/02/18 14:20
S15	376	(712/200).CCLS.	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2005/02/22 09:30
S16	139	(712/224).CCLS.	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2005/02/22 09:30
S17	238	(712/22).CCLS.	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2005/02/22 09:30
S18	378	(712/200).CCLS.	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2005/06/27 15:47
S19	141	(712/224).CCLS.	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2005/06/27 15:47
S20	251	(712/22).CCLS.	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2005/06/27 15:47
S21	0	(micro?engines) near4 (context adj1 event\$1) near4 (controller\$1) near4 (control adj1 stor\$4) near4 (transfer adj1 register\$1) near4 (register\$1) near4 (ALU\$1 or (arithmetic adj1 logic adj1 unit\$1) or execution\$1)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2005/06/27 16:00
S22	0	(micro?engines) same (context adj1 event\$1) same (controller\$1) same (control adj1 stor\$4) same (transfer adj1 register\$1) same (register\$1) same (ALU\$1 or (arithmetic adj1 logic adj1 unit\$1) or execution\$1)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2005/06/27 16:00

S23	0	(context adj1 event\$1) same (controller\$1) same (control adj1 stor\$4) same (transfer adj1 register\$1) same (register\$1) same (ALU\$1 or (arithmetic adj1 logic adj1 unit\$1) or execution\$1)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2005/06/27 16:00
S24	35	(context adj1 event\$1) and (controller\$1) and (control adj1 stor\$4) and (transfer adj1 register\$1) and (register\$1) and (ALU\$1 or (arithmetic adj1 logic adj1 unit\$1) or execution\$1)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2005/06/27 16:06
S25	17	(context adj1 event\$1) and (controller\$1) and (control adj1 stor\$4) and (transfer adj1 register\$1) and (register\$1) and (ALU\$1 or (arithmetic adj1 logic adj1 unit\$1) or execution\$1) and (shift\$3 near4 (operand\$1 or data or result\$1))	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2005/06/27 16:08
S26	1	("20020053017").PN.	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2005/06/28 15:44
S27	24	(multi?thread\$3) near4 (SMP or (symmetric adj1 multi?process\$3) or (cluster\$3 near4 (comput\$3 or process\$3)))	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2005/06/30 08:18
S28	37	(multi?thread\$3) with (SMP or (symmetric adj1 multi?process\$3) or (cluster\$3 near4 (comput\$3 or process\$3)))	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2005/06/30 08:18
S29	53	(multi?thread\$3) same (SMP or (symmetric adj1 multi?process\$3) or (cluster\$3 near4 (comput\$3 or process\$3)))	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2005/06/30 08:18



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S1	1	("20020053017").PN.	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2005/06/30 08:17
S2	0	(multiple near4 (ALU\$1 or (arithmetic adj1 logic adj1 unit\$1))) near4 (shift\$3 near4 (destination adj1 register\$1))	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2005/02/18 13:04
S3	3	((ALU\$1 or (arithmetic adj1 logic adj1 unit\$1))) near4 (shift\$3 near4 (destination adj1 register\$1))	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2005/02/18 13:13
S4	0	((multiple or plurality) near4 (ALU\$1 or (arithmetic adj1 logic adj1 unit\$1))) near4 (shift\$3 near4 (destination adj1 register\$1))	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2005/02/18 13:14
S5	22	((multiple or plurality) near4 (ALU\$1 or (arithmetic adj1 logic adj1 unit\$1))) near4 (shift\$3 near4 instruction\$1)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2005/02/18 13:15
S6	1	("4992934").PN.	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2005/02/18 13:47
S7	3641	shift adj1 mask	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2005/02/18 13:47
S8	1	(shift adj1 mask) near4 (4?bit\$1)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2005/02/18 13:49
S9	53	(shift near4 mask) near4 byte\$1	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2005/02/18 13:52
S10	1	(shift adj1 instruction\$1) near4 mask near4 byte\$1	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2005/02/18 14:03
S11	0	(shift adj1 instruction\$1) near4 multi?thread\$3	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2005/02/18 14:03